

\*\*\*Spare part\*\*\* SIMATIC S7-200, CPU 224XP Compact unit, AC power supply 14DI DC/10DO relay, 2 AI, 1 AO, 12/16 KB progr./10 KB data, 2 PPI/user-programmable interface



Figure similar

Supply voltage	
Rated value (AC)	
<ul style="list-style-type: none"> <li>120 V AC</li> <li>230 V AC</li> </ul>	<p>Yes</p> <p>Yes</p>
Load voltage L+	
<ul style="list-style-type: none"> <li>Rated value (DC)</li> <li>permissible range, lower limit (DC)</li> <li>permissible range, upper limit (DC)</li> </ul>	<p>24 V</p> <p>5 V</p> <p>30 V</p>
Load voltage L1	
<ul style="list-style-type: none"> <li>Rated value (AC)</li> <li>permissible range, lower limit (AC)</li> <li>permissible range, upper limit (AC)</li> <li>permissible frequency range, lower limit</li> <li>permissible frequency range, upper limit</li> </ul>	<p>100 V; 100 V AC to 230 V AC</p> <p>5 V</p> <p>250 V</p> <p>47 Hz</p> <p>63 Hz</p>
Input current	
Inrush current, max. from supply voltage L1, max.	20 A; at 264 V 220 mA; 35 to 100 mA (240 V); 70 to 220 mA (120 V); output current for expansion modules (5 V DC) 600 mA
Encoder supply	
24 V encoder supply	
<ul style="list-style-type: none"> <li>24 V</li> <li>Short-circuit protection</li> <li>Output current, max.</li> </ul>	<p>Yes; Permissible range: 20.4V to 28.8V</p> <p>Yes; electronic at 280 mA</p> <p>280 mA</p>
Power loss	
Power loss, typ.	11 W
Memory	
Number of memory modules (optional)	1; pluggable memory module, content identical with integral EEPROM; can additionally store recipes, data logs and other files
Work memory	
<ul style="list-style-type: none"> <li>integrated (for program)</li> <li>integrated (for data)</li> </ul>	<p>16 kbyte; 12 KB with active run-time edit</p> <p>10 kbyte</p>
Backup	
<ul style="list-style-type: none"> <li>present</li> </ul>	Yes; Program: Entire program maintenance-free on integral EEPROM, programmable via CPU; data: Entire DB 1 loaded from PG/PC maintenance-free on integral EEPROM, current values of DB 1 in RAM, retentive memory bits, timers, counters, etc. maintenance-free via high-performance capacitor; optional battery for long-term buffering
Battery	
Backup battery	
<ul style="list-style-type: none"> <li>Backup time, max.</li> </ul>	100 h; (min. 70 h at 40 °C); 200 days (typ.) with optional battery module

CPU processing times	
for bit operations, max.	0.22 µs
Counters, timers and their retentivity	
S7 counter	
• Number	256
Retentivity	
— adjustable	Yes; via high-performance capacitor or battery
— lower limit	1
— upper limit	256
Counting range	
— lower limit	0
— upper limit	32 767
S7 times	
• Number	256
Retentivity	
— adjustable	Yes; via high-performance capacitor or battery
— upper limit	64
Time range	
— lower limit	1 ms
— upper limit	54 min; 4 timers: 1 ms to 30 s; 16 timers: 10 ms to 5 min; 236 timers: 100 ms to 54 min
Data areas and their retentivity	
Flag	
• Size, max.	32 byte
• Retentivity available	Yes; M 0.0 to M 31.7
• of which retentive with battery	0 to 255, via high-performance capacitor or battery, adjustable
• of which retentive without battery	0 to 112 in EEPROM, adjustable
Hardware configuration	
Number of expansion units, max.	7; Only expansion modules of the S7-22x series can be used. Due to the limited output current, the use of expansion modules may be limited.
connectable programming devices/PCs	SIMATIC PG/PC, standard PC
Expansion modules	
• Analog inputs/outputs, max.	38; 2 onboard inputs and 1 output, also max. 28 inputs and 7 outputs (EM) or max. 0 inputs and 14 outputs (EM)
• Digital inputs/outputs, max.	168; max. 94 inputs and 74 outputs (CPU + EM)
• AS-Interface inputs/outputs, max.	62; AS-Interface A/B slaves (CP 243-2)
Digital inputs	
Number of digital inputs	14
Source/sink input	Yes; optionally, per group
Input voltage	
• Rated value (DC)	24 V
• for signal "0"	0V to 5V; 0V to 1V (I 0.3 to I 0.5)
• for signal "1"	min. 15 V; min. 4 V (I 0.3 to I 0.5)
Input current	
• for signal "1", typ.	2.5 mA; 8 mA for I 0.3 to I 0.5
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable	Yes; all
— at "0" to "1", min.	0.2 ms
— at "0" to "1", max.	12.8 ms
for interrupt inputs	
— parameterizable	Yes; I 0.0 to I 0.3
for technological functions	
— parameterizable	Yes; (E 0.0 to E 1.5) up to 200 kHz
Cable length	
• shielded, max.	500 m; Standard input: 500 m, high-speed counters: 50 m
• unshielded, max.	300 m; not for high-speed signals
Digital outputs	
Number of digital outputs	10; Relays
Short-circuit protection	No; to be provided externally
Switching capacity of the outputs	
• with resistive load, max.	2 A
• on lamp load, max.	200 W; 30 W with DC, 200 W with AC

<b>Output voltage</b>	
• for signal "1", min.	L+/L1
<b>Output current</b>	
• for signal "1" rated value	2 A
• for signal "0" residual current, max.	0 mA
<b>Output delay with resistive load</b>	
• "0" to "1", max.	10 ms; all outputs
• "1" to "0", max.	10 ms; all outputs
<b>Parallel switching of two outputs</b>	
• for uprating	No
<b>Switching frequency</b>	
• of the pulse outputs, with resistive load, max.	1 Hz
<b>Total current of the outputs (per group)</b>	
all mounting positions	
— up to 40 °C, max.	10 A
horizontal installation	
— up to 55 °C, max.	10 A
<b>Relay outputs</b>	
• Number of relay outputs	10
• Number of operating cycles, max.	10 000 000; mechanically 10 million, at rated load voltage 100 000
<b>Cable length</b>	
• shielded, max.	500 m
• unshielded, max.	150 m
<b>Analog inputs</b>	
Number of analog potentiometers	2; Analog potentiometer; resolution 8 bit
<b>Encoder</b>	
Connectable encoders	
• 2-wire sensor	Yes
— permissible quiescent current (2-wire sensor), max.	1 mA
<b>1. Interface</b>	
Interface type	Integrated RS 485 interface
Protocols	
• MPI	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s
• PPI	Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication ; transmission rates 9.6/19.2/187.5 kbit/s
• serial data exchange	Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter
MPI	
• Transmission rate, min.	19.2 kbit/s
• Transmission rate, max.	187.5 kbit/s
<b>2. Interface</b>	
Interface type	Integrated RS 485 interface
Protocols	
• MPI	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s
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<b>Integrated Functions</b>	
Number of alarm inputs	4; 4 rising edges and/or 4 falling edges
<b>Potential separation</b>	
Potential separation digital inputs	
• between the channels	Yes

<ul style="list-style-type: none"> <li>• between the channels, in groups of</li> </ul>	6 and 8
<b>Potential separation digital outputs</b>	
<ul style="list-style-type: none"> <li>• between the channels</li> <li>• between the channels, in groups of</li> </ul>	Yes; Relays 3 and 4
<b>Permissible potential difference</b>	
between different circuits	500 V DC between 24 V DC and 5 V DC; 1500 V AC between 24 V DC and 230 V AC
<b>Degree and class of protection</b>	
IP degree of protection	IP20
<b>Ambient conditions</b>	
<b>Ambient temperature during operation</b>	
<ul style="list-style-type: none"> <li>• horizontal installation, min.</li> <li>• horizontal installation, max.</li> <li>• vertical installation, min.</li> <li>• vertical installation, max.</li> </ul>	0 °C 55 °C 0 °C 45 °C
<b>Air pressure acc. to IEC 60068-2-13</b>	
<ul style="list-style-type: none"> <li>• permissible range, lower limit</li> <li>• permissible range, upper limit</li> </ul>	860 hPa 1 080 hPa
<b>Relative humidity</b>	
<ul style="list-style-type: none"> <li>• Operation, min.</li> <li>• Operation, max.</li> </ul>	5 % 95 %; RH class 2 in accordance with IEC 1131-2
<b>configuration / header</b>	
<b>configuration / programming / header</b>	
<ul style="list-style-type: none"> <li>• Command set</li> <li>• Program processing</li> <li>• Program organization</li> <li>• Number of subroutines, max.</li> </ul>	Bit logic instructions, compare instructions, timer instructions, counter instructions, clock instructions, transmissions instructions, table instructions, logic instructions, shift and rotate instructions, conversion instructions, program control instructions, interrupt and communications instructions, logic stack instructions, integer maths, floating-point math instructions, numerical functions  free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms) 1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer 64
<b>Programming language</b>	
<ul style="list-style-type: none"> <li>— LAD</li> <li>— FBD</li> <li>— STL</li> </ul>	Yes Yes Yes
<b>Know-how protection</b>	
<ul style="list-style-type: none"> <li>• User program protection/password protection</li> </ul>	Yes; 3-stage password protection
<b>connection method / header</b>	
Plug-in I/O terminals	Yes
<b>Dimensions</b>	
Width	140 mm
Height	80 mm
Depth	62 mm
<b>Weights</b>	
Weight, approx.	440 g
<b>last modified:</b>	3/12/2021 